



IFW

Docket No.: MUH-12728

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

By: _____ Date: March 2, 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No.	: 10/631,350	Confirmation No: 1926
Applicant	: Hans Weber, et al.	
Filed	: July 31, 2003	
Art Unit	: 2818	
Examiner	: Dao H. Nguyen	
Title	: Method for Forming a Channel Zone of a Transistor and NMOS Transistor	
Docket No.	: MUH-12728	
Customer No.	: 24131	
Date of Notice of Allowance	: December 2, 2005	

LETTER

Commissioner for Patents,
P.O. Box 1450, Alexandria, VA 22313-1450

Sir:

Please note that the German Priority document number 102 35 000.0 dated July 31, 2002 was filed on September 25, 2003 in the U.S Patent Office. Please find enclosed copies of the following:

- 1) Claim for Priority
- 2) The cover page of priority document; and
- 3) The return postcard indicating receipt by the PTO.

Respectfully submitted,

Ralph E. Locher
Reg. No. 41,947

Date: March 2, 2006

Lerner and Greenberg, P.A.
Post Office Box 2480
Hollywood, FL 33022-2480
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Fax: (954) 925-1101

/mj



Docket No.: MUH-12728

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313 20231.

By: 

Date: September 23, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hans Weber et al.
Appl. No. : 10/631,350
Filed : July 31, 2003
Title : Method for Forming a Channel Zone of a Transistor and PMOS Transistor

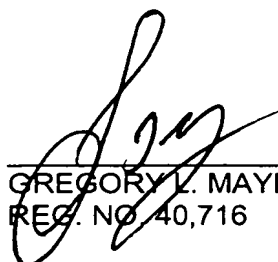
CLAIM FOR PRIORITY

Hon. Commissioner for Patents,
Alexandria, VA 22313-1450
Sir:

Claim is hereby made for a right of priority under Title 35, U.S. Code, Section 119, based upon the German Patent Application 102 35 000.0 filed July 31, 2002.

A certified copy of the above-mentioned foreign patent application is being submitted herewith.

Respectfully submitted,



GREGORY L. MAYBACK
REG. NO. 40,716

Date: September 23, 2003

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/mjb



Prioritätsbescheinigung über die Einreichung einer Patentanmeldung

Aktenzeichen: 102 35 000.0

Anmeldetag: 31. Juli 2002

Anmelder/Inhaber: Infineon Technologies AG, München/DE

Bezeichnung: Verfahren zur Bildung einer Kanalzone eines Transistors und PMOS-Transistor

IPC: H 01 L 21/336

Die angehefteten Stücke sind eine richtige und genaue Wiedergabe der ursprünglichen Unterlagen dieser Patentanmeldung.

München, den 11. Juli 2003
Deutsches Patent- und Markenamt
Der Präsident
Im Auftrag

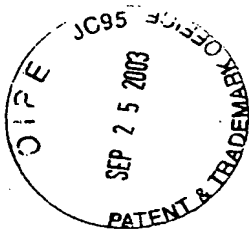


CKET NO. MUH-12728 Mailed Sept. 23, 2003

APPLIC. NO. 10/631,350 Express Mail

The stamp of the Patent Office hereon may be considered the date on which papers indicated below were received.

Applic pgs . . . Rule 53b New ☐ Contin ☐ Div ☐ CIP ☐ /Rule 53c Prov. ☐ /Rule 53d CPA . . . RCE ☐
☐ CIP . . . pgs ☐ Design ☐ . . . Dwgs ☒ Declaration ☒ Mailing Certif.
☒ Priority Claim ☒ Cert. Prior. Doc(s) ☐ PCT Cover Sheet WO . . .
☐ Amend pgs . . . ☐ Prel. Amend pgs . . . ☒ Letter
☐ Response pgs . . . ☐ 37CFR1.116 ☐ Not. of Appeal
☐ Brief pgs . . . ☐ Appndx pgs . . . ☐ I.D.S. + . . . Refs.
☐ Assoc Pwr of Atty ☒ Credit Card \$ / 30
☐ Pet. for Ext . . . Mo. ☐ Pet . . . ☐ Check \$. . .
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(Patent Office. Please stamp and return to addressee on reverse side.)